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(Approved by **AICTE**, New Delhi & Recognized by **UGC** under section **2(f) & 12(B)**)
(Permanently affiliated to JNTUK, Kakinada, Accredited by **NBA** and **NAAC** with '**A**' Grade)
Pedatadepalli, **TADEPALLIGUDEM** – **534 101.**W.G.Dist. (**A.P**)

Department of Electronics and Communication Engineering

Ref No: SVEC/ECE/Circular/2023-24/34 Date: 09-09-2023

CIRCULAR

This is to inform to all faculty and students of ECE department that, we are going to conduct Engineer Day Celebrations-2K23. In this regard, students are encouraged to participate in the Events. Following are the details of Technical Events & faculty Coordinators. Interested students can contact concerned faculty coordinators.

Sl. No.	Name of the Committee/Event	Faculty Coordinator	Date / Time	Venue
1.	POSTER ARENA	Dr. M. THAMARAI Sri. B. MURALI KRISHANA Smt. M. NEELIMA Sri D. VENKANNA BABU	13-09-2023 (10 AM to 1 PM)	EDC Lab
2.	TRICKY LOGIC	Smt. Y.SUJATHA Dr. S.V.V. SATYANARAYANA Sri P.GOPALA REDDY	13-09-2023 (2:30 PM to 4:30 PM)	ECAD Lab
3.	CIRCUITHON	Dr. PURNIMA K SHARMA Sri P.V.V.SATYANARAYANA Sri J. RAJENDRA Ms. K. DURGA SARANYA	14-09-2023 (10 AM to 1 PM)	LICA Lab
4.	TECHNICAL QUIZ	Sri S. KAMESH Sri R.L.R LOKESH BABU Sri R. RAM PRASAD Ms. P. HARSHINI	14-09-2023 (1:45 PM to 2:45 PM) Prelims 15-09-2023 (9:45 AM to 11:45 AM) Finals	Seminar Hall
5.	STARTUP- SPARK	Dr. T.V.N.L. ASWINI Sri T.SREENIVASU Sri K. PASIPALANA RAO	14-09-2023 (2 PM to 4:15 PM)	R&D Lab
6.	PROJECT EXPO	Sri KNH SRINIVAS Dr. T.D.N.S.S. SARVESWARARAO Sri M. VENKATA SUMAN Sri P.V.V. RAJESH Sri V. ANIL KUMAR	15-09-2023 (11:45 AM to 1:30 PM)	ECAD Lab
7.	Certificates & Prizes	Dr. M.KOTESWARA RAO Sri THOTA SREENIVAS Sri M.SUBBA RAO Sri M. VINOD KUMAR		

Note:

- 1. No Registration Fee
- 2. Each Student can participate in any Two Events.
- 3. Prizes & Certificates will be there.
- 4. Detailed Guidelines & Registration links will be given soon and also will be displayed in College Website
- *** Expecting Active Participation from Students to make the Program Grand Success.

Head of the Department

E. Kusmaline



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Department of Electronics and Communication Engineering

Event wise Guidelines for Engineer's day - 2K23 under VEDA , Technical Association

1. EVENT-1: POSTER ARENA:

Team Size: 1(Individual)

Theme of the event: Technological solution for a societal problem through Poster.

No of rounds in the event: 1 Brief description of round:

Create a poster that is visually engaging and informative based on the theme/ solution for the given problem. Clearly present the root causes and solutions in diagrammatic form with few key points.

General Guidelines:

- Every participant should register through Google form link.
- Last date for registration is 12/09/2023. Size of the team: one (Individual).
- The theme for poster arena event will be given on 13th September morning at 9.30 a.m. and the event starts at 10.00 a.m.
- The Event duration is 3 hrs (10 AM to 1 PM).
- Participant should bring white color drawing sheet and necessary stationery items for presenting their ideas/solutions in poster form.
- Unique number will be allotted to each participant, so, it is not necessary to mention participant name or any signature in the poster.

At the end of event, poster should be displayed on the allocated place for evaluation purpose.

Event-2: TECHNICAL QUIZ:

Team size: Maximum 4 members per team

No. of rounds: Two (Prelims, Finals)

Brief description:

All the registered teams are allowed to attend the round-I (Prelims).

Round-I (Prelims) on 14-09-2023 (A.N.):

It contains two sections internally.

Section-I contains multiple choice questions that covers the ECE related subjects.

Section-II is a rapid fire round where the questions are asked based on the picture shown.

From round-I, highly scored six teams will move to round-II (Finals).

Round-II (Finals) on 15-09-2023 (F.N.):

It contains two sections internally and both are raid fire type.

Section-I contains the questions related to ECE subjects along with 'C' language.

Section-II contains the questions relevant to Chandrayaan-III.

Finally, the top scored three teams are selected as winners.

General guidelines:

- 1. Only the registered members are allowed to participate as teams.
- 2. There is no spot registration.
- 3. The team member may be from different years preferably.
- 4. Participants are not allowed to bring any electronic gadgets to the event.
- 5. 30 seconds time is allotted to answer for each question.
- 6. The evaluator's decision is final for awarding the marks.
- 7. In case of a tie between two teams, an additional question(s) may decide the winner.

Event -3: CIRCUITHON:

Size of the Team: 3 members

Theme of the Event: Circuit Innovation and Building Number of Rounds in the Event: Three Rounds

Brief Description of Rounds:

Round 1: Component-Based Innovation

Participants are required to select components available in the lab.

Within the stipulated time duration, participants must create an innovative circuit of their choice.

Creativity and functionality will be assessed.

Round 2: Theme-Based Circuit Design

Student batches who qualified in the first round will proceed to this round.

Participants are given a specific theme for circuit design.

They must prepare circuits based on the specified theme.

Creativity, adherence to the theme, and functionality will be evaluated.

Round 3: Final Circuit Building

Four batches will be selected from the previous round to compete in the final round.

In this round, circuits will be shown to the participants.

Participants must recreate the circuits on a breadboard within a 10-minute time duration.

The output for the circuits is also required.

Speed, accuracy, and circuit functionality will be assessed.

The top performers will be declared winners.

General Guidelines:

Participants must adhere to safety guidelines while handling components and equipment.

Participants should maintain the integrity of the competition and respect fellow participants.

Judges' decisions are final and binding.

EVENT-4: TRICKY LOGIC:

Team Size: 3

Theme of the event: VLSI Magic -Where Logic Becomes Reality

No. of rounds in the event: 2

Round 1: Duration: 15 Minutes, Team has to draw the logic diagram for the given problem. Round 2: Duration: 30 Minutes, Team has to develop VHDL/ VERILOG Code for the given

problem and verify its functionality

General guidelines:

Should not carry any electronics gadgets

Every round is evaluated by expert committee and their decision is Final

Event 5: START-UP SPARK

General guidelines:

- 1. Allowed as a team participation only. Maximum allowed members for team should be Two.
- 2. Preferably team members should be from different years of under graduation students.
- 3. The Start-up idea can be given in PPT/ Module/ Poster.
- 4. The idea should focus technically to the problem they have chosen.
- 5. A duration of 10 minutes is given to the team to present their idea.
- 6. Top Three positions will be announced according to Secured rank.
- 7. Evaluator's decision is final.

Event 6: PROJECT EXPO

- 1. Team size should be less than 5 members.
- 2. Prepare a chart for presentation.
- 3. Prepare your own project kit.
- 4. All Project Batch Students should be available at the time of Presentation
- 5. Project functionality and Novelty will be assessed.
- 6. The top performers will be declared winners.
- 7. Participants must adhere to safety guidelines while handling components and equipment.
- 8. Participants should maintain the Healthy Competitive, Sportive Spirit
- 9. Judges' decisions are final and binding.

To Register for the above Events use the below Link: https://forms.gle/dAfLhVfJq1HjKLw76

Head of the Department

E. Kumalini

Vision

To develop the department into a centre of excellence and produce high quality, technically competent and responsible Electronics and communicationengineers

Mission

- To create a learner centric environment that promotes the intellectual growth of thestudents.
- To develop linkages with R & D organizations and educational institutions for excellence in teaching, learning and consultancypractices.
- To build the student community with high ethicalstandards.



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Department of Electronics and Communication Engineering

Engineer's Day Celebrations - 2K23 Winners & Runners List

1. Poster Arena

		1st Place		
Sl. No.	Roll Number	Name of the Student	Year & Section	
1.	20A81A0409	D. Nkitha	IV Year B.Tech-ECE-A	

		2 nd Place		
Sl. No.	Roll Number	Name of the Student	Year & Section	
1.	21A81A0460	T. N. V. Harish	III Year B.Tech-ECE-A	

		3 rd Place		
Sl. No.	Roll Number	Name of the Student	Year & Section	
1.	22243-EC-037	P. Jai Deep	II Year Diploma	

2. Tricky Logic

1 st Place			
Sl. No.	Roll Number	Name of the Student	Year & Section
1.	21A81A0449	R. Hari Venkata Santhosh	III Year B.Tech-ECE-A
2.	21A81A0447	P.D.S.L. Keerthi	III Year B.Tech-ECE-A
3.	21A81A0412	D. B. Murali Krishna	III Year B.Tech-ECE-A

		2nd Place	
Sl. No.	Roll Number	Name of the Student	Year & Section
1.	21A85A0412	M. Kaarthik	IV Year B.Tech-ECE-B
2.	21A85A0414	T. Sai Ramarao	IV Year B.Tech-ECE-B
3.	21A85A0415	B. Sai Durga	IV Year B.Tech-ECE-B

3 rd Place			
Sl. No.	Roll Number	Name of the Student	Year & Section
1.	21A81A04F0	G. Prathap	III Year B.Tech-ECE-C
2.	22A85A0417	S.P.V.L. Gayathri	III Year B.Tech-ECE-C
3.	22A85A0418	V. Prasanna Kumari	III Year B.Tech-ECE-C



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3. Circuithon

		1st Place	
Sl. No.	Roll Number	Name of the Student	Year & Section
1.	23A85A0418	G. Pavan Manikanta Kumar	II Year B.Tech-ECE-C
2.	21A81A0443	N. Uma Sai Durga	III Year B.Tech-ECE-A
3.	22A81A04E5	D. Uma Satya Kumari	II Year B.Tech-ECE-C
4.	21243-EC-058	U. Vijaya Raja Sri	III Year Diploma

2 nd Place			
Sl. No.	Roll Number	Name of the Student	Year & Section
1.	21A81A04F0	G. Prathap	III Year B.Tech-ECE-C
2.	22243-EC-037	P. Jai Deep Balaji	II Year Diploma
3.	23A85A1406	S. Sowmika	II Year B.Tech-ECT
4.	22A81A1450	P. Y. Sri Harshitha	II Year B.Tech-ECT

		3 rd Place	
Sl. No.	Roll Number	Name of the Student	Year & Section
1.	21A81A0426	K. H. Navya	III Year B.Tech-ECE-A
2.	23A85A0425	M. Santhosh Kumar	II Year B.Tech-ECE-C
3.	22A81A04G5	K. Ikyata	II Year B.Tech-ECE-C
4.	21243-EC-023	K. Soma Sekhara Reddy	III Year Diploma

4. Startup-SPARK

1 st Place			
Sl. No.	Roll Number	Name of the Student	Year & Section
1.	20A81A04B0	P. Pavani	IV Year B.Tech-ECE-B
2.	20A81A0486	K. Sujatha	IV Year B.Tech-ECE-B

2 nd Place			
Sl. No.	Roll Number	Name of the Student	Year & Section
1.	22A81A04E7	E. Hema	II Year B.Tech-ECE-C
2.	22A81A04I9	Y. Hima Eswar	II Year B.Tech-ECE-C



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3 rd Place			
Sl. No.	Roll Number	Name of the Student	Year & Section
1.	22A81A1445	P. Sudarshan	II Year B.Tech-ECT
2.	22A81A1443	P. V. Pavan Kumar	II Year B.Tech-ECT

5. Technical Quiz

1 st Place			
Sl. No.	Roll Number	Name of the Student	Year & Section
1.	23A85A0416	B. Rajesh	II Year B.Tech-ECE-C
2.	23A85A0424	M. L. P. Seethayya Naidu	II Year B.Tech-ECE-C
3.	23A85A0418	G. P. Manikanta Kumar	II Year B.Tech-ECE-C

2 nd Place			
Sl. No.	Roll Number	Name of the Student	Year & Section
1.	22A85A0414	A.N.V.S. Durga Anjali	III Year B.Tech-ECE-C
2.	22A85A0417	S.P.V.L. Gayathri	III Year B.Tech-ECE-C
3.	22A85A0418	V. Prasanna Kumari	III Year B.Tech-ECE-C
4.	21A81A14F0	G. Prathap	III Year B.Tech-ECE-C

		3 rd Place	
Sl. No.	Roll Number	Name of the Student	Year & Section
1.	22A81A0482	U. Gayathri	II Year B.Tech-ECE-B
2.	22A81A04D0	U. Manasa Lakshmi	II Year B.Tech-ECE-B
3.	22A81A04G4	K. Lalitha Malliswari	II Year B.Tech-ECE-C
4.	22A81A04I8	V. V. S Lakshmi	II Year B.Tech-ECE-C



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6. Project Expo

1st Place			
Sl. No.	Roll Number	Name of the Student	Year & Section
1.	21A81A0467	A.J.V. Surendra Narayana	III Year B.Tech-ECE-B
2.	21A81A0482	G. Suvarana Raju	III Year B.Tech-ECE-B
3.	22A85A0409	K. Siva Sai Teja	III Year B.Tech-ECE-B
4.	22A85A0410	N. Ajaya Sai Mahesh	III Year B.Tech-ECE-B

2 nd Place			
Sl. No.	Roll Number	Name of the Student	Year & Section
1.	21A81A0470	A.H.S.V.S. Vamsi	III Year B.Tech-ECE-B
2.	21A81A0473	B. Bhavani Sankaram	III Year B.Tech-ECE-B
3.	21A81A0486	K. Chiru Vaibhav Reddy	III Year B.Tech-ECE-B
4.	21A81A04A5	N. Raj Charan	III Year B.Tech-ECE-B

3 rd Place			
Sl. No.	Roll Number	Name of the Student	Year & Section
1.	21243-EC-016	G. Raju	III Year Diploma
2.	21243-EC-063	Y.N.S. Kishore Babu	III Year Diploma
3.	22243-EC-045	S.H.V. Siva Ganesh	II Year Diploma
4.	22243-EC-064	Y. Bhanu Venkat	II Year Diploma

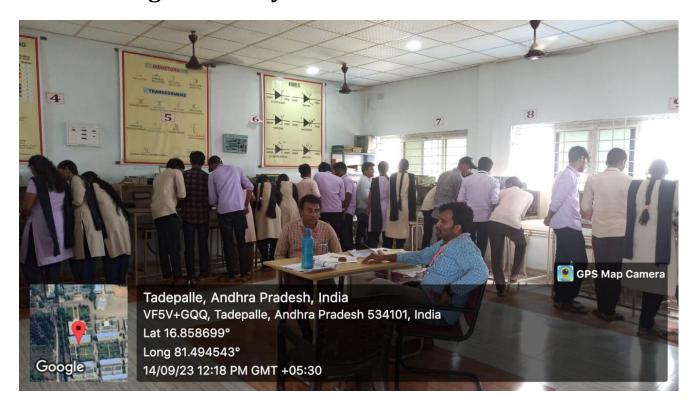


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Engineer's Day Celebrations-2K23 Photos







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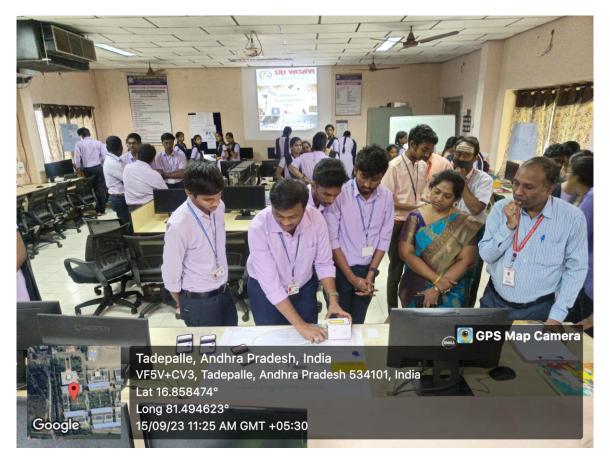






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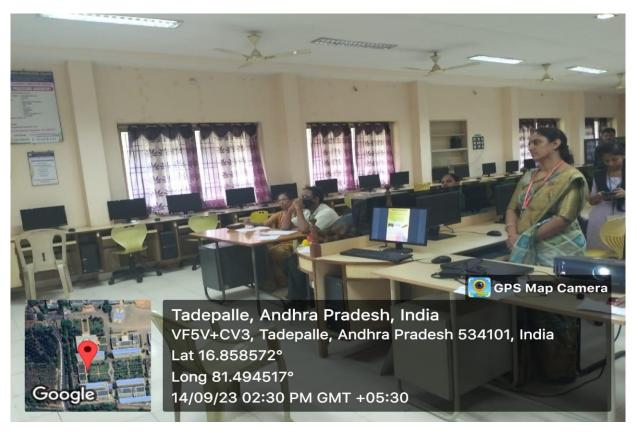






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